

S/N 09/382,929

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Paul A. Farrar Examiner: David Graybill
Serial No.: 09/382,929 Group Art Unit: 2827
Filed: August 25, 1999 Docket: 303.603US1
Title: PACKAGING OF ELECTRONIC CHIPS WITH AIR-BRIDGE STRUCTURES

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicants have included the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p). Please charge any additional fees or credit any overpayment to Deposit Account No. 19-0743.

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The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

PAUL A. FARRAR

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 371-2109

Date

July 14, 2004

By

Danny J. Pady
Reg. No. 35,635

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 14th day of July, 2004.

Peter Rebuffoni
Name

Peter Rebuffoni
Signature

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>	Complete if Known <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 40%;">Application Number</td> <td>09/382,929</td> </tr> <tr> <td>Filing Date</td> <td>August 25, 1999</td> </tr> <tr> <td>First Named Inventor</td> <td>Farrar, Paul</td> </tr> <tr> <td>Group Art Unit</td> <td>2827</td> </tr> <tr> <td>Examiner Name</td> <td>Graybill, David</td> </tr> </table>	Application Number	09/382,929	Filing Date	August 25, 1999	First Named Inventor	Farrar, Paul	Group Art Unit	2827	Examiner Name	Graybill, David
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Group Art Unit	2827										
Examiner Name	Graybill, David										
Sheet 1 of 1	Attorney Docket No: 303.603US1										

US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
	US-6,433,413	08/13/2002	Farrar, P. A.	257	678	08/17/2001

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²

EXAMINER

DATE CONSIDERED



SN 09/382,929

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Paul A. Farrar

Examiner: David Graybill

Serial No.: 09/382,929

Group Art Unit: 2827

Filed: August 25, 1999

Docket: 303.603US1

Title: PACKAGING OF ELECTRONIC CHIPS WITH AIR-BRIDGE STRUCTURES

COMMUNICATION CONCERNING RELATED APPLICATION(S)

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
10/179,110	June 24, 2002	303.610US2	INSULATORS FOR HIGH DENSITY CIRCUITS
10/179,091	June 24, 2002	303.610US3	A MEMORY SYSTEM WITH CONDUCTIVE STRUCTURES EMBEDDED IN FOAMED INSULATOR
10/179,151	June 24, 2002	303.610US4	INSULATORS FOR HIGH DENSITY CIRCUITS

Respectfully submitted,

PAUL A. FARRAR

By Applicant's Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938Minneapolis, MN 55402
(612) 371-2109

Date

July 14, 2004

By

Danny J. Padys

Reg. No. 35,635

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Peter Rebuffoni
Name

Peter Padys
Signature